



MAR 03 2004

TRANSMITTAL OF FORMAL DRAWINGS

Docket No.
FIS920030411US1

Inventor: **Hiroyuki Akatsu, et al.**

Serial No.	Filing Date	Confirmation No.	Examiner	Art Unit
10/708,340	2/25/04			

Invention: **STRUCTURE AND METHOD OF SELF-ALIGNED BIPOLAR TRANSISTOR HAVING TAPERED COLLECTOR**

Address to:
Commissioner for Patents
P.O. Box 1450
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Transmitted herewith are:

10 sheets of formal drawing(s) for this application.

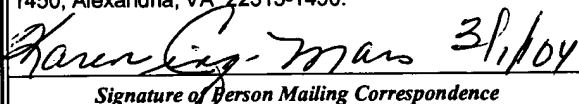
☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).


Signature

H. DANIEL SCHNURMANN
AGENT
REG. NO.: 35,791

Dated: **MARCH 1, 2004**

I certify that this document and attached formal drawings are being deposited on **MARCH 1, 2004** with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


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KAREN CINQ-MARS

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